

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Naoto Horiguchi

Serial Number: Not Yet Assigned

Filed: March 23, 2004

For: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Attorney Docket No. 042261
Customer No.: 38834

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

March 23, 2004

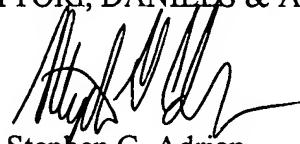
Sir:

In compliance with 37 CFR 1.56, Applicant(s) call(s) to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

Copies of the references are enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge
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Respectfully submitted,
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Enclosures: PTO-1449; Reference (2)
SGA/ll

**INFORMATION
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PTO-1449**

Atty. Docket No. 042261 Serial No. New Appln.

Applicant(s): Naoto HORIZUCHI

Filing Date: March 23, 2004 Group Art Unit:

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
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____ AA
____ AB
____ AC
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FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
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____ AE	2003-031798	01/31/03	Japan	Abstract & Discussed in the Spec.
____ AF	6-224381	08/12/94	Japan	Abstract & Discussed in the Spec.
____ AG				
____ AH				
____ AI				

OTHER DOCUMENTS

____ AJ
____ AK

Examiner

Date Considered